10053761_CLSTITLES
Titles of Most Frequently Occurring Classifications of Patents Returne

From A Search of 10053761 on July 16, 2004

4	326/86 Class 326/62 326/82 326/83 326/86		OR, 2 XR) : ELECTRONIC DIGITAL LOGIC CIRCUITRY INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT, ETC.) .Current driving (e.g., fan in/out, off chip driving, etc.)Field-effect transistorBus driving
4	327/261 Class 327/100 327/261	327	OR, 4 XR) : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS SIGNAL CONVERTING, SHAPING, OR GENERATING . Having specific delay in producing output waveform
3			OR, 2 XR) : ELECTRONIC DIGITAL LOGIC CIRCUITRY SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY .Bus or line termination (e.g., clamping, impedance matching, etc.)
3		327	OR, 1 XR) : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS SIGNAL CONVERTING, SHAPING, OR GENERATING .Synchronizing
3	327/292 Class 327/100 327/291 327/292	327	OR, 2 XR) : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS SIGNAL CONVERTING, SHAPING, OR GENERATING .Clock or pulse waveform generatingClock fault compensation or redundant clocks
3	327/407 Class 327/365 327/407	(1 327	OR, 2 XR) : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS GATING (I.E., SWITCHING INPUT TO OUTPUT) .Converging with plural inputs and single output

3		365	10053761_CLSTITLES OR, 2 XR) : STATIC INFORMATION STORAGE AND RETRIEVAL INTERCONNECTION ARRANGEMENTS
3		713	OR, 1 XR) : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: SUPPORT CLOCK CONTROL OF DATA PROCESSING SYSTEM, COMPONENT, OR DATA TRANSMISSION
3	714/699	714	OR, 2 XR) : ERROR DETECTION/CORRECTION AND FAULT DETECTION/RECOVERY PULSE OR DATA ERROR HANDLING .Skew detection correction
2		307	OR, 2 XR) : ELECTRICAL TRANSMISSION OR INTERCONNECTION SYSTEMS NONLINEAR REACTOR SYSTEMS (E.G., SATURABLE) .Logic circuitsClocking, delay or transmission line
2		326	OR, 2 XR) : ELECTRONIC DIGITAL LOGIC CIRCUITRY SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY
2		326	OR, 2 XR) : ELECTRONIC DIGITAL LOGIC CIRCUITRY TRI-STATE (I.E., HIGH IMPEDANCE AS THIRD STATE
2	327/154 Class 327/100 327/141 327/154	327	OR, 2 XR) : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS SIGNAL CONVERTING, SHAPING, OR GENERATING .SynchronizingWith feedforward
2	327/161 Class 327/100 327/141 327/161	327	OR, 2 XR) : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS SIGNAL CONVERTING, SHAPING, OR GENERATING .SynchronizingWith delay means
2	327/231	(0	OR, 2 XR)

)

			10052761 GLGWTWIDG
	Class	327	10053761_CLSTITLES : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
	327/100 327/231		SIGNAL CONVERTING, SHAPING, OR GENERATING .Phase shift by less than period of input
2	327/258 Class		OR, 2 XR) : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR
	327/100 327/231 327/258		DEVICES, CIRCUITS, AND SYSTEMS SIGNAL CONVERTING, SHAPING, OR GENERATING .Phase shift by less than period of inputMultiple outputs
2	327/259 Class		OR, 2 XR) : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS SIGNAL CONVERTING, SHAPING, OR GENERATING .Phase shift by less than period of inputMultiple outputsNon-overlapping
	327/100 327/231 327/258 327/259		
2	347/237 Class	(1 347	OR, 1 XR) : INCREMENTAL PRINTING OF SYMBOLIC INFORMATION
	347/224 347/225 347/233 347/237		LIGHT OR BEAM MARKING APPARATUS OR PROCESSES .Scan of lightPlural beam scanDriving circuitry
2		365	OR, 1 XR) : STATIC INFORMATION STORAGE AND RETRIEVAL READ/WRITE CIRCUIT
2		375	OR, 0 XR) : PULSE OR DIGITAL COMMUNICATIONS REPEATERS
2	375/354	375	OR, 1 XR) : PULSE OR DIGITAL COMMUNICATIONS SYNCHRONIZERS .Network synchronizing more than two stations
2	377/64 Class 377/64	377	OR, 1 XR) : ELECTRICAL PULSE COUNTERS, PULSE DIVIDERS, OR SHIFT REGISTERS: CIRCUITS AND SYSTEMS SHIFT REGISTER
2	713/400		

A,

10053761_CLSTITLES
Class 713: ELECTRICAL COMPUTERS AND DIGITAL PROCESSING

SYSTEMS: SUPPORT

713/400 SYNCHRONIZATION OF CLOCK OR TIMING SIGNALS,

DATA, OR PULSES